

产品规格书

产品名称：液晶显示屏

型 号：JUN4330PT-7

日 期：2019-5-20

版 本：NV4044-V00

品质部	工程部	审核
施振业	何志新	何志新

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1. General Description

This main Module has a 4.3 inch diagonally measured active display area with 480(RGB)X272 resolution. Each pixel is divided into Red, Green and Blue sub-pixels and dots which are arranged in vertical stripes.

LCD color is determined with 262,000 colors signal for each pixel.

The HT043STDA3007-R0 has been designed to apply the interface method that enables low power, high speed, and high contrast.

The HT043STDA3007-R0 is intended to support applications where thin thickness, wide viewing angle and low power are critical factors and graphic displays are important.

2. General Features

Item	Display Panel	Remark
Display Mode	Normally White, Transmissive LCD	
Viewing Direction	12 O'CLOCK	
Input Signals	RGB	
Outside Dimensions	105.5mm(W)*67.2mm(H)*3.0mm(T)	
Effective Area	-	
Active Area	95.04mm(W)×53.856mm(H)	
Number of Pixels	480×RGB×272Pixels	
Pixel Pitch	0.198mm(H) × 0.198mm(W)	
Pixel Arrangement	RGB Vertical stripes	
Driver IC		

3. Absolute Maximum Ratings

The following are maximum values which, if exceeded may cause operation or damage to the unit.

ITEM	Symbol	Min.	Typ.	Max.	Unit	Remark
Power for Circuit Driving	VDD		3.2		V	
Power for Circuit Logic	VCI		3.2		V	
LC Operating Voltage *1)	Vop		3.3		V	
LED Forward Voltage	V _f	-	22.4	-	V	
LED Forward Current	I _r	-	20	-	mA	
LCD Luminance	B _p	-	280	-	cd/m ²	
Storage Humidity	H _{ST}	10	-	90	%RH	At 25±5 °C
Storage Temperature	T _{ST}	-30	-	80	°C	
Operating Ambient Humidity	H _{OP}	10	-	90	%RH	
Operating Ambient temperature	T _{OP}	-20	-	70	°C	

Note:

- *1) Liquid Crystal driving voltage.
Due to the characteristics of LC Material, this voltage vary with environmental temperature.
- *2) Temp. >60°C, Absolute humidity shall be less than 90%RH at 60°C
- *3) Temp. ≤60°C, 90%RH MAX.

4. Electrical Specification Main Window Display

(Unless specified, the ambient temperature Ta=25°C)

Properties	Sym.	Min	Typ.	Max	Unit	Note
Power for Circuit Driving	VDD		3.2		V	Note
Power for Circuit Logic	VCI		3.2		V	Note
BLU Driving Logic	Vbat	-	-	-	V	
Logic Input Voltage	Low Voltage	VIL	0	-	0.2VDD	V
	High Voltage	VIH	0.8VDD	-	VDD	V
Logic Output Voltage	Low Voltage	VOL	0	-	0.1VDD	V
	High Voltage	VOH	0.9VDD	-	VDD	V
Power Consumption	White	P _w	T.B.D	T.B.D	T.B.D	mW
	Black	P _b	T.B.D	T.B.D	T.B.D	mW
	Vertical Stripe	P _v	T.B.D	T.B.D	T.B.D	mW

Note:

The recommended operating conditions refer to a range in which operation of this product is guaranteed. Should this range is exceeded, the operation cannot be guaranteed even if the values may be without the absolute maximum ratings.

Accordingly, please make sure that the module is used within this range. And these current values are measured under the condition that all devices are stopped, each component is stable and logic signal is input.

5. Optical Specification

ITEM	SYMBOL	CONDITION	Min.	TYP.	Max.	
Color Filter Chromacicity (Note.1)	White	x	$\theta = \phi = 0^\circ$	0.287	0.307	0.327
		y		0.321	0.341	0.361
		Y		29.0	32.0	35.0
	Red	x	$\theta = \phi = 0^\circ$	0.633	0.653	0.673
		y		0.312	0.332	0.352
		Y		15.55	18.55	21.55
	Green	x	$\theta = \phi = 0^\circ$	0.294	0.314	0.334
		y		0.555	0.575	0.595
		Y		58.71	61.71	64.71
	Blue	x	$\theta = \phi = 0^\circ$	0.117	0.137	0.157
		y		0.113	0.133	0.153
		Y		13.79	15.79	18.79
Transmittance(%) (Note.3)	T	$\theta = \phi = 0^\circ$	--	5	--	

Note.1 These items are measured by C light.

Note.2 Definition of Viewing Angle(θ, ψ), refer to Fig.1 as below :

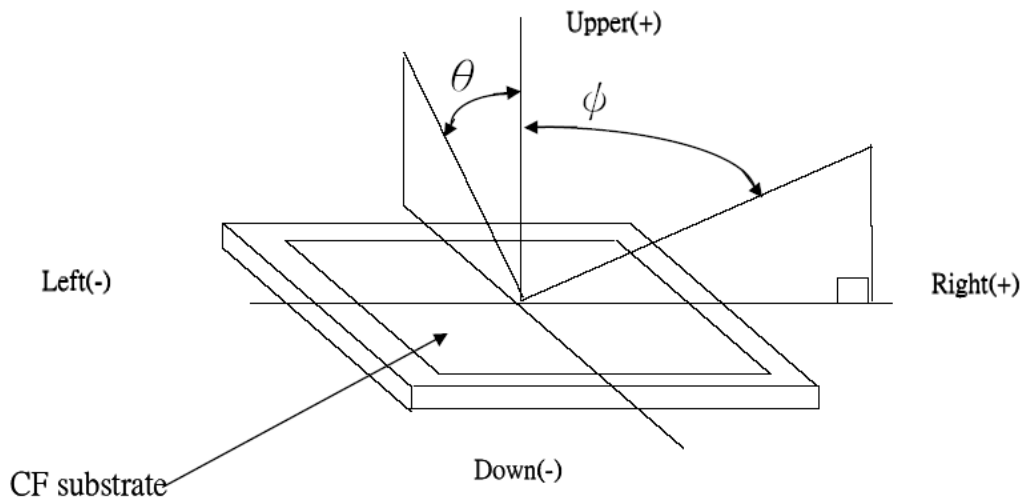
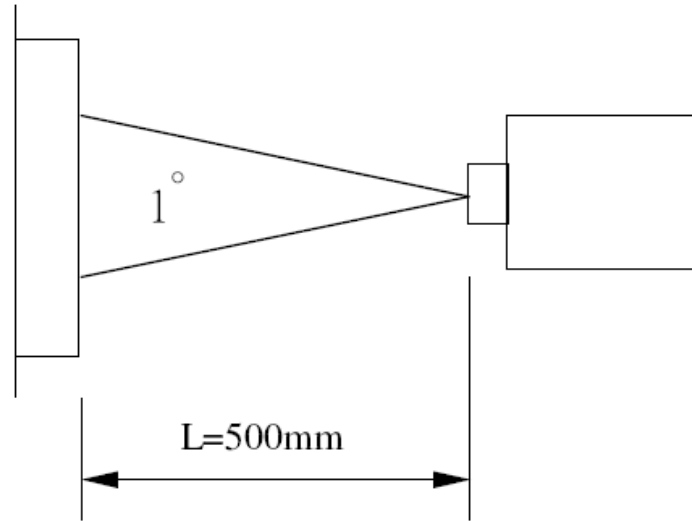
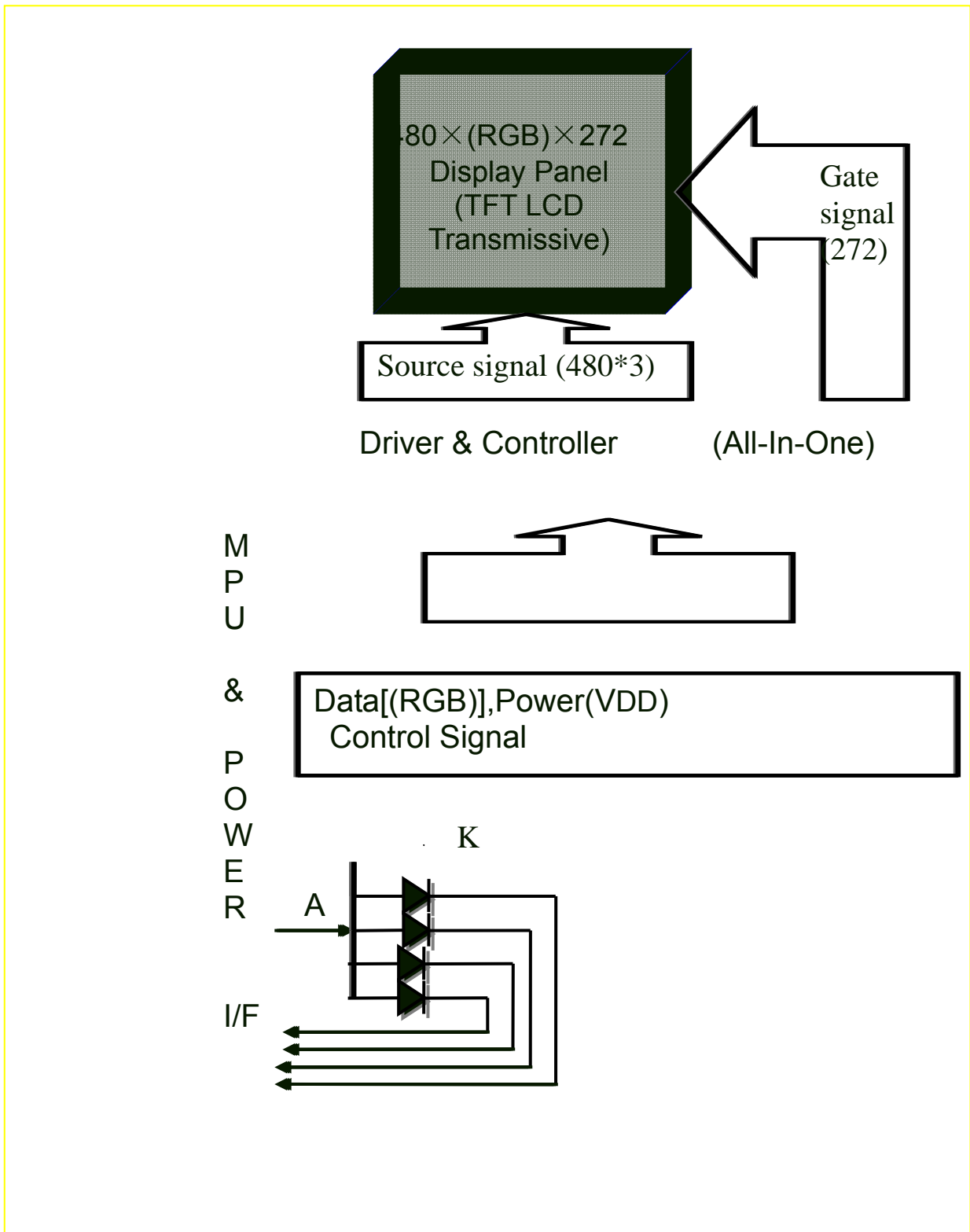


Fig.1 Definition of Viewing Angle

Note.3 Using LC+ EWV Polarizer+Corresponding Backlight, reference only, Measure device : BM-5A (TOPCON) , viewing cone= 1° , $I_L=20mA$.



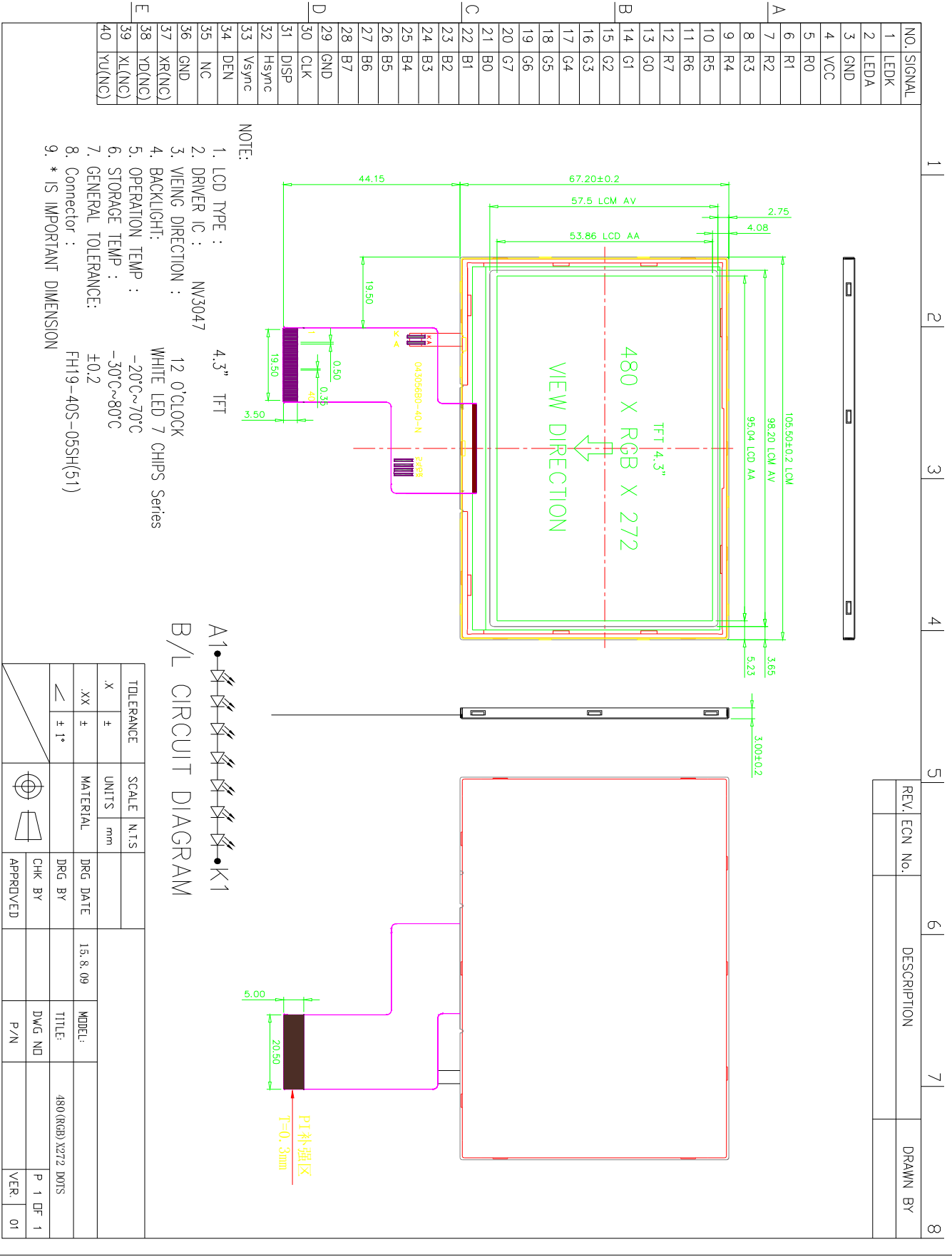
6. Block Diagram



7. Pin Description

Pin NO.	Symbol	Description
1	LEDK	LED BACKLIGHT(CATHODE
2	LEDA	LED BACKLIGHT(ANODE
3	GND	GROUND
4	VCC	POWER SUPPLY
5	R0	RED DATA
6	R1	RED DATA
7	R2	RED DATA
8	R3	RED DATA
9	R4	RED DATA
10	R5	RED DATA
11	R6	RED DATA
12	R7	RED DATA
13	G0	GREEN DATA
14	G1	GREEN DATA
15	G2	GREEN DATA
16	G3	GREEN DATA
17	G4	GREEN DATA
18	G5	GREEN DATA
19	G6	GREEN DATA
20	G7	GREEN DATA
21	B0	BLUE DATA
22	B1	BLUE DATA
23	B2	BLUE DATA
24	B3	BLUE DATA
25	B4	BLUE DATA
26	B5	BLUE DATA
27	B6	BLUE DATA
28	B7	BLUE DATA
29	GND	GROUND
30	CLK	CLOCK SIGNAL
31	DISP	DISPLAY ON/OFF
32	HSYNC	HORIZONTAL SYNC INPUT IN RGB MODE
33	VSYNC	VERTICAL SYNC INPUT IN RGB MODE
34	DEN	DATA ENABLE
35	NC	NC
36	GND	GROUND
37	XR(NC)	TOUCH PLANE PIN/NC
38	YD(NC)	TOUCH PLANE PIN/NC
39	XL(NC)	TOUCH PLANE PIN/NC
40	YU(NC)	TOUCH PLANE PIN/NC

8. Outline Dimension



1	2	3	4	5	6	7	8
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REV.	ECN No.	DESCRIPTION	DRAWN BY
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A1
B/L CIRCUIT DIAGRAM

TOLERANCE	SCALE	N.T.S	DRG. DATE	15.8.09	MODEL:	TITLE:	480 (RGB) X272 0001S
X ±	UNITS	mm	DRG. BY		DWG. NO		P 1 DF 1
XX ±			CHK. BY				VER. 01
± 1°			APPROVED		P/N		

9. Timing Characteristics

9.1.1. Parallel 24-bit RGB Timing Table

Item	Symbol	Min.	Typ.	Max.	Unit	Remark	
DCLK Frequency	Fclk	8	9	12	MHz		
DCLK Period	Tclk	83	111	125	ns		
HSYNC	Period Time	Th	485	531		DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	30	43	64	DCLK	By H_Blanking setting
	Front Porch	Thfp	2	8	64	DCLK	
	Pulse Width	Thw	2	4		DCLK	
VSYNC	Period Time	Tv	276	292		H	
	Display Period	Tvdisp		272		H	
	Back Porch	Tvbp	2	12	64	H	By V_Blanking setting
	Front Porch	Tvfp	2	8	64	H	
	Pulse Width	Tvw	2	4		H	

Note: It is necessary to keep Tvbp=12, Tvfp = 8, Tw = 4 and Thbp = 43, Thfp = 8 Thw = 4 in sync mode.

9.2. Timing Characteristics

Normal Write Mode(HWM='0'), IOVcc=1.65V~3.6V,Vcc=2.5V~3.6V

Parameter	Symbol	Unit	Min.	Max.	Unit
Bus cycle time write	t _{CYCW}	ns	100	-	-
Bus cycle time read	t _{CYCR}	ns	160	-	-
Write low-level pulse width	PW _{LR}	ns	35	-	-
Read low-level pulse width	PW _{LR}	ns	45	-	-
Write high-level pulse width	PW _{HR}	ns	35	-	-
Read high-level pulse width	PW _{HR}	ns	90	-	-
Write/Read rise/fall time	t _{WR,WRf}	ns	-	-	25
Setup time Write	t _{AS}	ns	0	-	-
Setup time Read	t _{AS}	ns	10	-	-
Address hold time	t _{AH}	ns	2	-	-
Write data setup time	t _{DSW}	ns	25	-	-
Write data hold time	t _H	ns	5	-	-

Read data delay time	t _{DDR}	ns	-	-	100
Read data hold time	t _{DHR}	ns	5	-	-

9.3 Reset Operation

(VCC=1.65~3.1 V)

Table 13-6

Item	Symbol	Unit	Min.	Typ.	Max.
Reset low-level width	t _{RES}	ms	1	-	-
Reset rise time	tr _{RES}	μs	-	-	10

10. Reliability and Inspection Standard

No.	Test Item	Test Conditions	Remark	
1	High Temperature	Storage	70°C, 120Hr	Note
		Operation	60°C, 120Hr	Note
2	Low Temperature	Storage	-30°C, 120Hr	Note
		Operation	-20°C, 120Hr	
3	High Temperature and High Humidity	60°C, 90%RH, 120Hr	Note	
4	Temperature Cycle	Storage	-10°C(1Hr)→25°C(5min)→60°C(1Hr) 32 Cycles	Note
		Operation	-20°C(1Hr)→25°C(5min)→60°C(1Hr) 25 Cycles	
5	Peeling Off (Storage)	≥ 500gf/cm	Note	
6	FPC Bending Test	≥ 6,000 times, 2/sec	Note	
7	Vibration Test(Storage)	50HZ, 30min, Amplitude: 2 cm, X/Y/Z directions	Note	
8	Drop Test	60cm/ 3Corner/ 8Face, 1Cycle	Note	
9	Electrostatic Discharge	+/-200V,200pf(0ohm) 1 time/each terminal		

Note:

- 1) The test samples should be applied to only one test item.
- 2) Sample size for each test item is 5~10pcs.
- 3) For Damp Proof Test, pure water(Resistance>1MΩ) should be used.
- 4) In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5) EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with

humidity and fluorescence EL has.

- 6) After the reliability test, the test samples should be inspected after 2 hours at least.
- 7) Functional test is OK. Missing segment, shorts, unclear segment, non display, display abnormally, liquid crystal leak are not allowed.
- 8) After testing, the current Idd should be within initial value $\pm 20\%$.
- 9) No low temperature bubbles ,end seal loose and fall, frame rainbow, ACF bubble growing are allowable in the appearance test.

11. Inspection Criterion

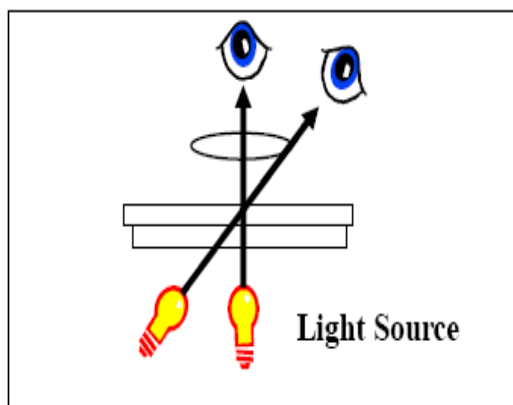
11.1. Sampling Method

Unless otherwise agreed upon in writing, the sampling inspection shall be applied to the Customer's incoming inspection.

- 1) Lot size: Quantity per shipment lot
- 2) Sampling type: Normal inspection , single sampling
- 3) Inspection level: II
- 4) Sampling table: MIL-STD-105D
- 5) Acceptable Quality Level(AQL): Major=0.65 Minor=1.5

11.2. Inspection Method

- 1) Ambient Condition:
 - a. Temperature: Room temperature $25\pm 5^{\circ}\text{C}$
 - b. Illumination: Single fluorescent lamp non-directive(300 to 700 Lux)
- 2) Viewing distance
The distance between the LCD and the inspector' s eyes shall be at least 30-50cm.
- 3) Viewing Angle
The inspection shall be conducted within normal viewing angle range.



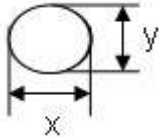

12.3. Inspection Criteria

12.3.1. Major defect

No.	Item	Inspection Standard	Classification of defects
1	All functional	1) No display 2) Display abnormally	Major

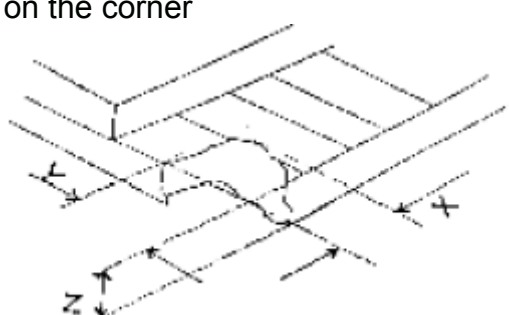
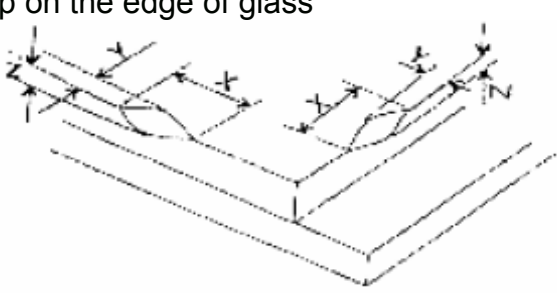
	defects	3) Open or missing segment 4) Short circuit 5) Excess power consumption 6) Backlight no lighting, flickering and abnormal lighting	
2	Missing	Missing component	Major
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	Major

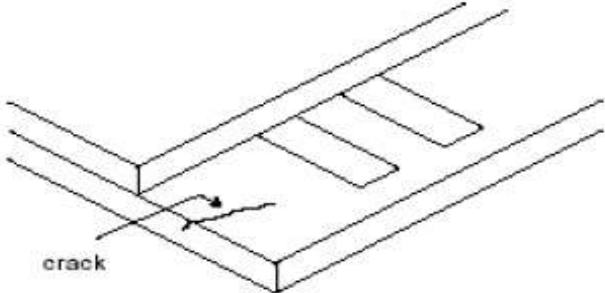
12.3.2. Cosmetic Defect

No.	Item	Inspection Standard		Classification of defects							
1	(spot defect) Black and White spot pinhole	For dark/white spot, size Φ is defined as $\Phi=(x+y)/2$		Minor							
		<table border="1"> <thead> <tr> <th>Size Φ (mm)</th> <th>Acceptable Quantity</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.1$</td> <td>Ignore</td> </tr> <tr> <td>$0.10 \leq \Phi \leq 0.15$</td> <td>2</td> </tr> <tr> <td>$0.15 \leq \Phi \leq 0.2$</td> <td>1</td> </tr> <tr> <td>$0.2 < \Phi$</td> <td>0</td> </tr> </tbody> </table>	Size Φ (mm)		Acceptable Quantity	$\Phi \leq 0.1$	Ignore	$0.10 \leq \Phi \leq 0.15$	2	$0.15 \leq \Phi \leq 0.2$	1
Size Φ (mm)	Acceptable Quantity										
$\Phi \leq 0.1$	Ignore										
$0.10 \leq \Phi \leq 0.15$	2										
$0.15 \leq \Phi \leq 0.2$	1										
$0.2 < \Phi$	0										
2	(line defect) Black and White line Polarizer scratch	Define: 		Minor							
		<table border="1"> <thead> <tr> <th>Width(mm)</th> <th>Length(mm); Acceptable Qty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.03$</td> <td>Ignore</td> </tr> <tr> <td>$0.03 < W \leq 0.05$</td> <td>$L \leq 3.0$; $N \leq 2$</td> </tr> <tr> <td>$0.05 < W \leq 0.1$</td> <td>$L \leq 2.0$; $N \leq 2$</td> </tr> <tr> <td>$0.1 < W$</td> <td>Define as spot defect</td> </tr> </tbody> </table>	Width(mm)		Length(mm); Acceptable Qty	$\Phi \leq 0.03$	Ignore	$0.03 < W \leq 0.05$	$L \leq 3.0$; $N \leq 2$	$0.05 < W \leq 0.1$	$L \leq 2.0$; $N \leq 2$
Width(mm)	Length(mm); Acceptable Qty										
$\Phi \leq 0.03$	Ignore										
$0.03 < W \leq 0.05$	$L \leq 3.0$; $N \leq 2$										
$0.05 < W \leq 0.1$	$L \leq 2.0$; $N \leq 2$										
$0.1 < W$	Define as spot defect										

3	Polarizer defect	Dent or bubble(between the polarizer and glass)		Minor
		Size Φ (mm)	Acceptable Qty	
		$\Phi \leq 0.10$	Ignor	
		$0.10 < \Phi \leq 0.20$	2	
		$0.20 < \Phi \leq 0.30$	1	
	$0.30 < \Phi$	0		

12.3.3. Cosmetic Defect

No.	Item	Inspection Standard	Classification of defects						
1	Glass defect	1) Chip on the corner  <table border="1" data-bbox="438 1288 1141 1400"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>≤ 3.0</td> <td>$\leq S$</td> <td>$\leq T$</td> </tr> </table> <p>Remark: S=contact pad length; T=the thickness of glass Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal. Acceptable Quantity $N \leq 2$.</p>	X	Y	Z	≤ 3.0	$\leq S$	$\leq T$	Minor
		X	Y	Z					
≤ 3.0	$\leq S$	$\leq T$							
		2) Chip on the edge of glass  <table border="1" data-bbox="438 1948 1141 2004"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> </table>	X	Y	Z	Minor			
X	Y	Z							

		Ignore	≤ 0.5	$\leq T$	
		Acceptable Quantity: $N \leq 2$			
		3) Creak Creaks tend to break are not allowed.			
					Minor

■ PRECAUTIONS FOR USING LCD MODULES

Handling Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in contact with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol
Do not scrub hard to avoid damaging the display surface.
- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents
Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.
- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is

accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
 - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist LCM.